## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
KUMAR ET AL.

Serial No. 10/611,322

Filing Date: JULY 1, 2003

For: DIGITAL ELECTRONIC CIRCUIT
FOR TRANSLATING HIGH VOLTAGE
LEVELS TO LOW VOLTAGE LEVELS

## TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop: PGPUB-Drawings Commissioner for Patents Alexandria, VA 22313-1450

Sir:

Enclosed are fourteen (14) sheets of formal drawings to be filed in the above-identified patent application.

Respectfully submitted,

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